

# Leif Clark

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## Education

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**MIT – Bachelor of Science in Electrical Engineering – GPA: 3.36/4.0** **Fall 2019 – Spring 2023**

- Studied design and troubleshooting of both analog and digital circuits in addition to power electronics, signal processing, computer architecture, and electromagnetic applications.
- Minored in political science

**Highlands College of Montana Technological University** **Fall 2016 – Spring 2019**

- Non-degree student of business administration, web design, MS Excel, English composition, Spanish, physics, and chemistry.

## Work Experience

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**Lead Web Developer, Human Security Dashboard Project, Allston, MA** **Summer 2021 – Fall 2022**

- Designed and implemented a website to display human security data in a minimalistic HTML5 compliant format with integrated SEO. Documented project to facilitate continuity of operations.

**Undergraduate Researcher on Multiple Projects, MIT, Cambridge, MA** **2020 - 2022**

- Compiled database of US nanofabrication infrastructure
- Specified and configured hardware for 16-channel active noise cancelation project
- Conducted extensive review of available information on government contracting processes with special emphasis on PMCs and DOD outsourcing

**Production Specialist, Ray Holes Leather Care Inc., Butte, MT** **Spring 2016 – Summer 2019**

- Produced, packaged and shipped leather care products with great independence
- Assisted in administrative work and equipment maintenance
- Adhered to a strict non-disclosure agreement

## Projects

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**Passive (Time-Independent) Radiolocation**

- Fabricated 50 Ohm microstrips and quarter wave dipole antennas to operate in the 2-3 GHz range
- Wrote python script to predict location of conductive objects in proximity to antennas based off of S11 and S12 VNA magnitude measurements

**Analog, Solid State evaluation of Vacuum Tubes**

- Designed and fabricated an analog circuit to divide the magnitudes of two AC signals by leveraging peak detection, log properties, and exponential approximations
- Conducted extensive simulation and verification in SPICE and identified causes of circuit performance deviation from simulation predictions

## Skills

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**Computer** Proficient in HTML, CSS, Python, LTspice, and SystemVerilog. Highly experienced with MS office suite and Scribus Desktop Publishing software. Some experience with RISC-V assembly, Bluespec Verilog, R, JavaScript, CAD, Inkscape, and RawTherapee.

**Technical** Licensed technician-class amateur radio operator (KJ7UQZ). Significant experience in electronics lab. Experienced in fundamentals of audio, video, and photo editing. Extensive experience in long-exposure photography and some background in strobe photography.

**Other** Strong organizational skills and ability to follow protocols. OSHA-10 certified and committed to maintaining a safe work environment. Extensive public speaking experience.